

AMENDMENT TRANSMITTAL LETTER

Docket No.
M4065.0014/P014 BApplication No.
09/940,791Filing Date
August 29, 2001Examiner
K. RoseArt Unit
2822

Applicant(s): Larry D. Kinsman

Invention: LOW PROFILE BALL GRID ARRAY PACKAGE

TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	6	- 20 =	x	0.00	
Independent Claims	2	- 3 =	x	0.00	
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00

 Large Entity Small Entity No additional fee is required for this amendment. Please charge Deposit Account No. _____ in the amount of \$ _____. A duplicate copy of this sheet is enclosed. A check in the amount of \$ _____ to cover the filing fee is enclosed. Payment by credit card. Form PTO-2038 is attached. The Commissioner is hereby authorized to charge and credit Deposit Account No. 04-1073 as described below. A duplicate copy of this sheet is enclosed. Credit any overpayment. Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.

Dated: July 3, 2002

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HSKB
Docket No.: M4065.0014/P014-B
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Larry D. Kinsman

Application No.: 09/940,791

Group Art Unit: 2822

Filed: August 29, 2001

Examiner: K. Rose

For: LOW PROFILE BALL GRID ARRAY
PACKAGE

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AMENDMENT

Box Non-Fee Amendment
Commissioner for Patents
Washington, DC 20231

Dear Sir:

This paper is responsive to the Office Action dated April 5, 2002, rejecting claims 39-44, Applicant amends the above-captioned application as follows:

IN THE CLAIMS:

Please replace claims 39 and 42 with amended claims 39 and 42.

Susy J-1
39. (amended) A processor system comprising:

a central processing unit; and

b
a memory device connected to said central processing unit, said memory device comprised of a plurality of low profile ball grid array semiconductor packages, said low profile ball grid array semiconductor packages comprised of a base substrate having a top surface and a bottom surface, with an aperture therein which extends from said top surface to said bottom surface,